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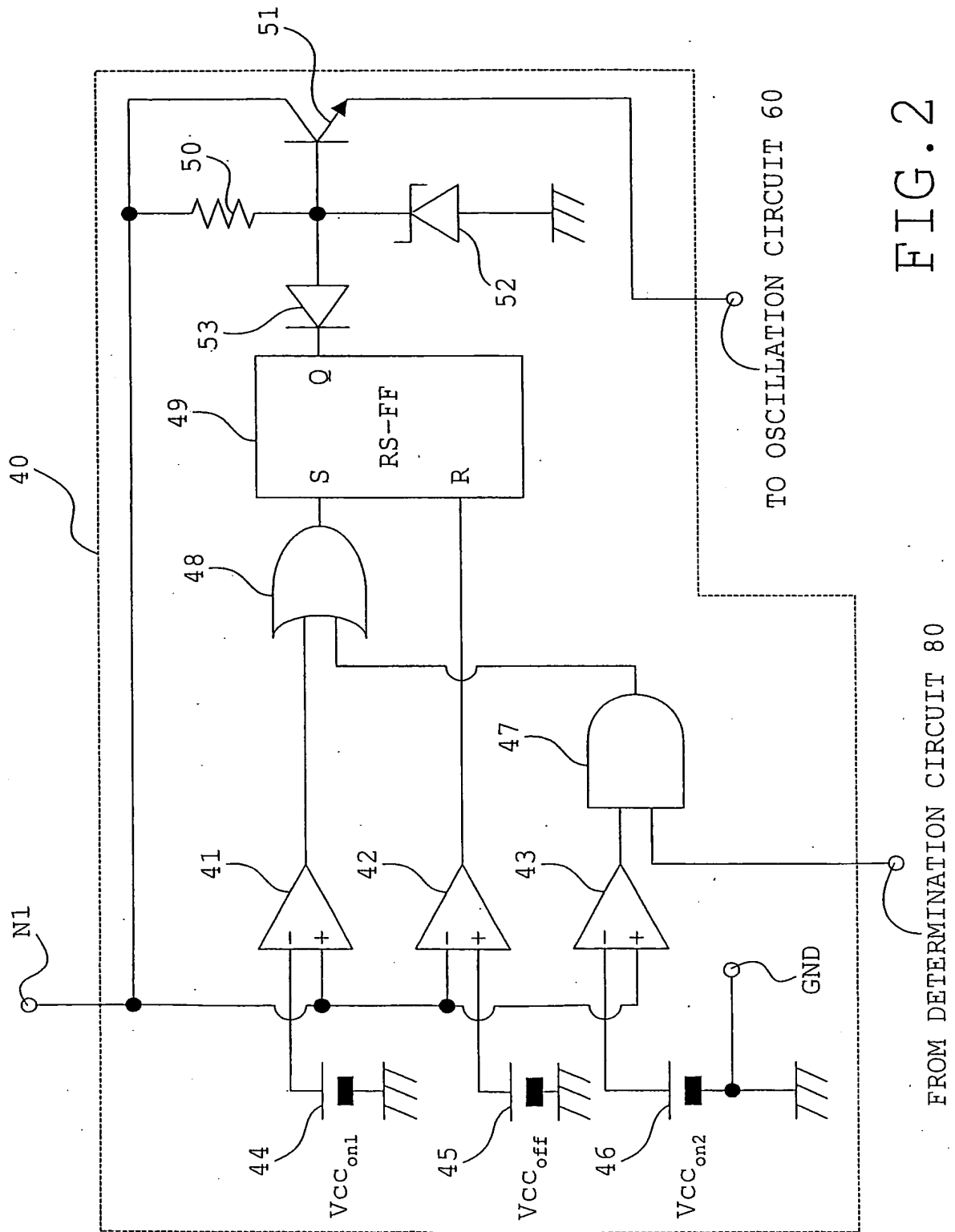


FIG. 2

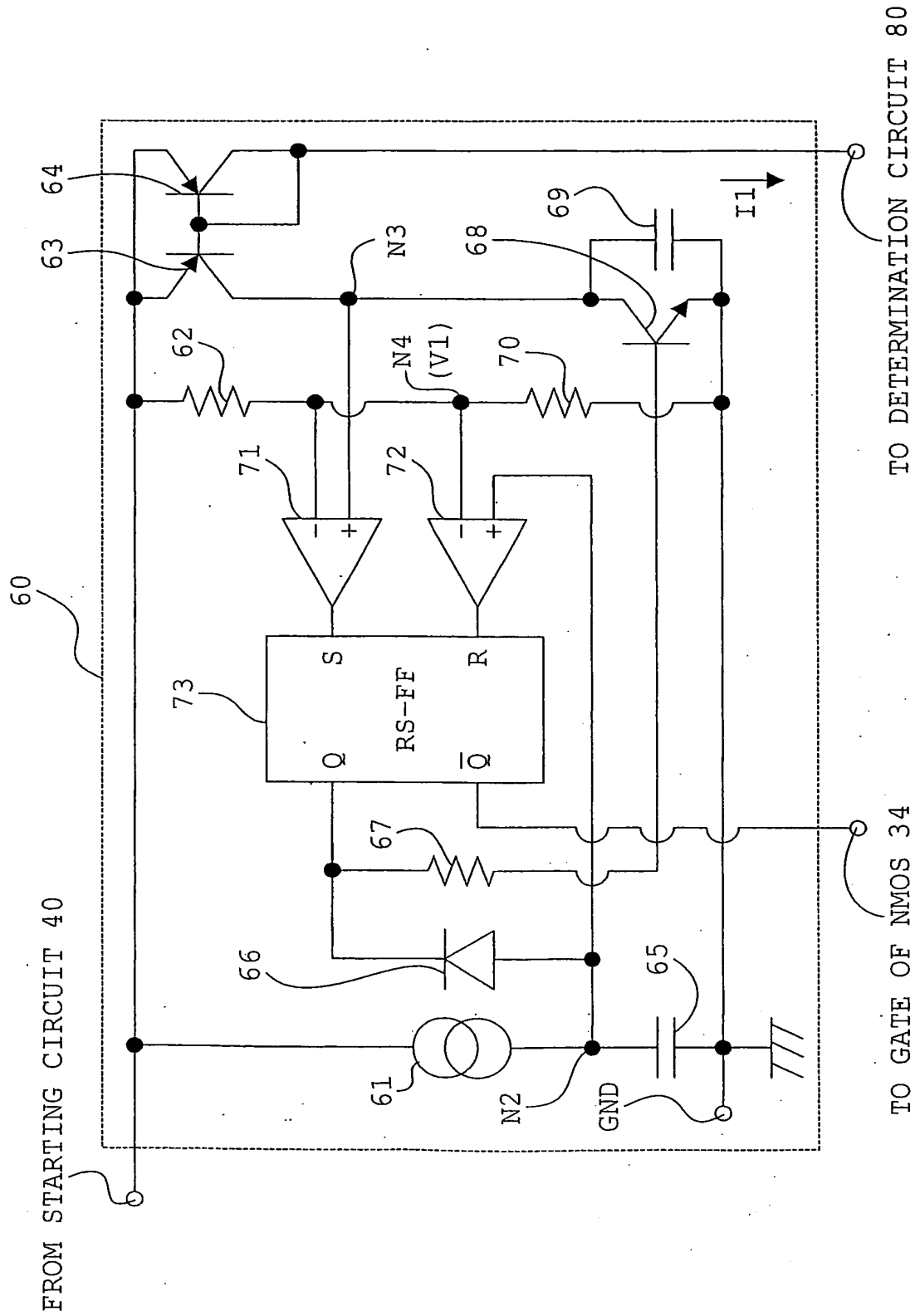
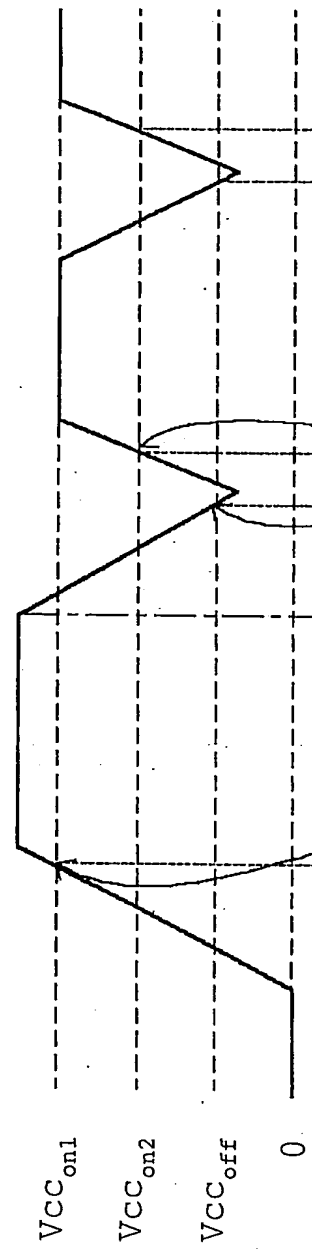
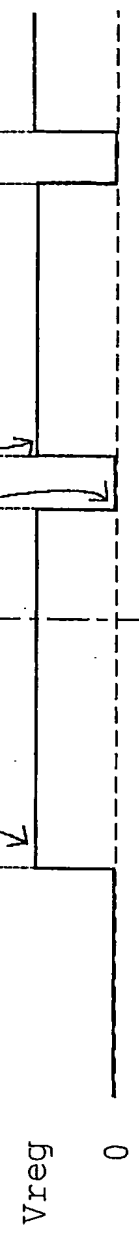


FIG. 3

(a) VOLTAGE OF NODE N1



(b) OUTPUT VOLTAGE OF TRANSISTOR 51



(c) DETERMINATION SIGNAL FROM DETERMINATION CIRCUIT 80

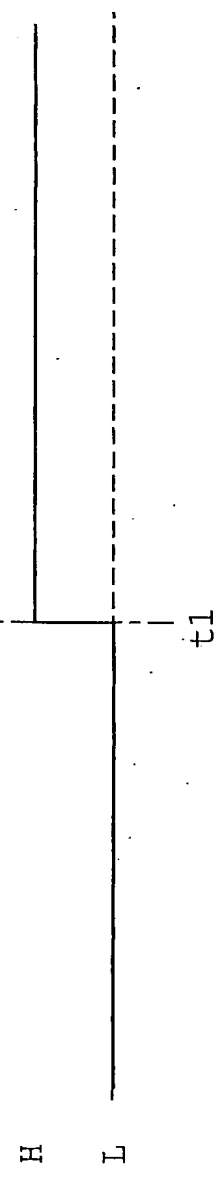


FIG. 4

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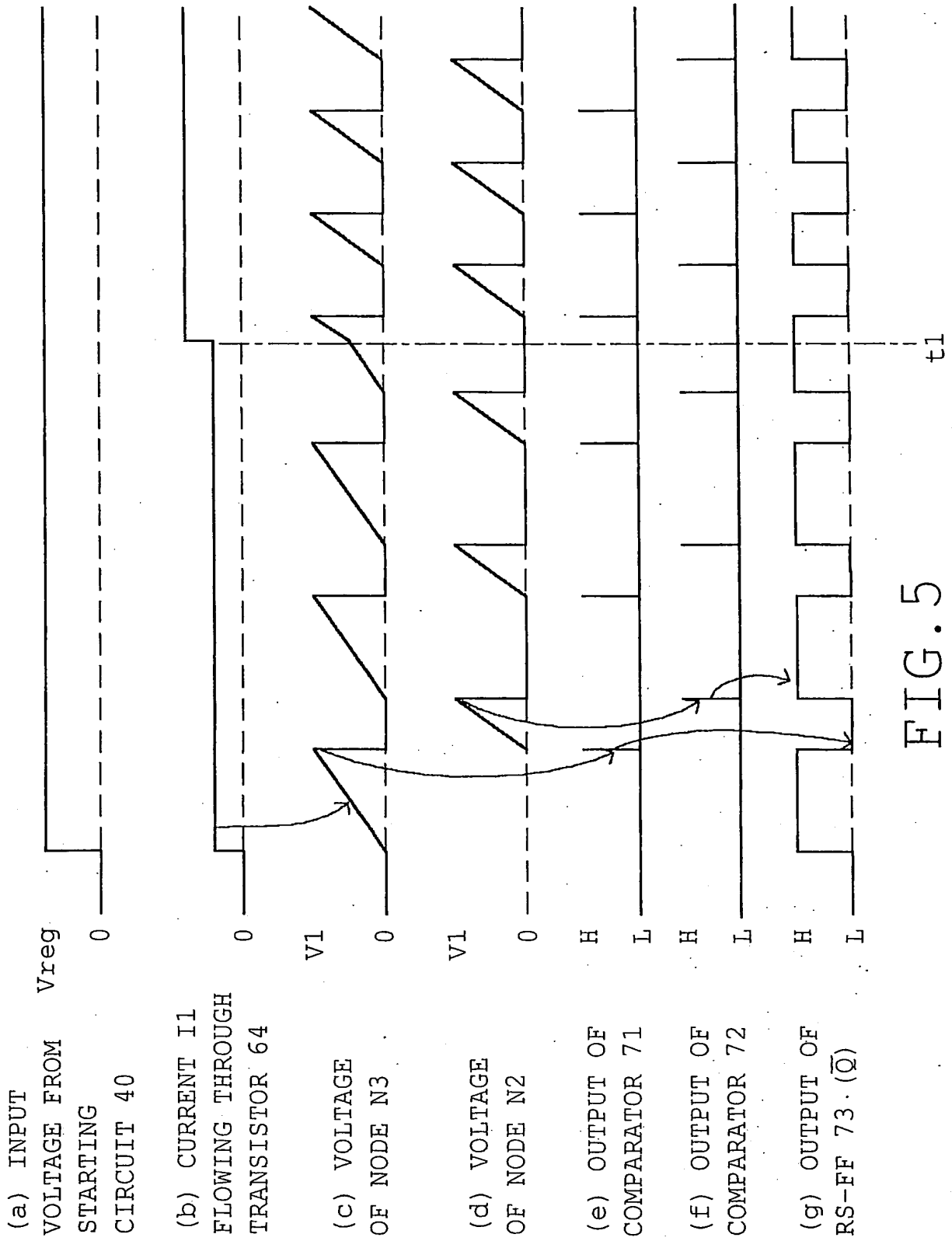


FIG. 5

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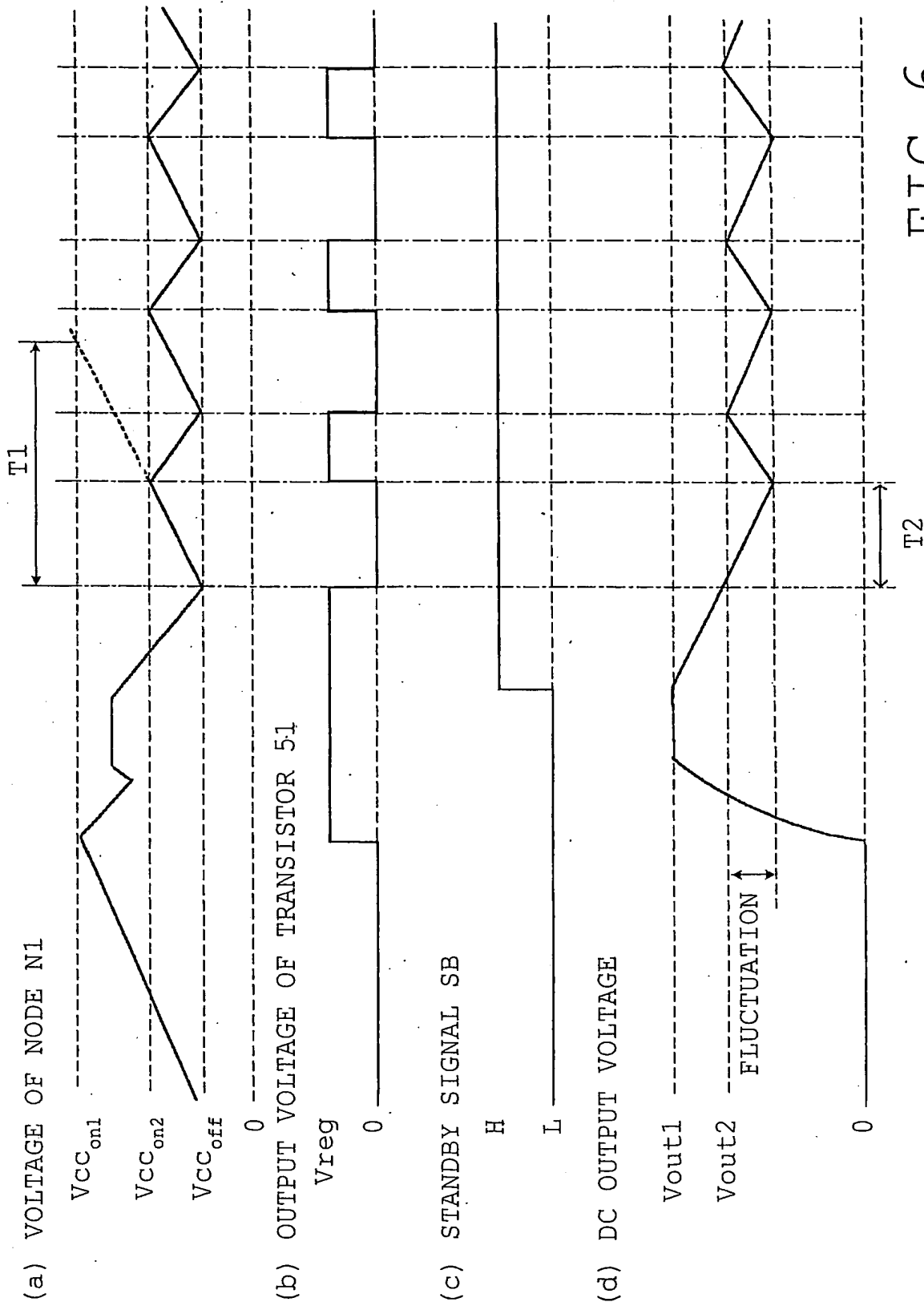


FIG. 6

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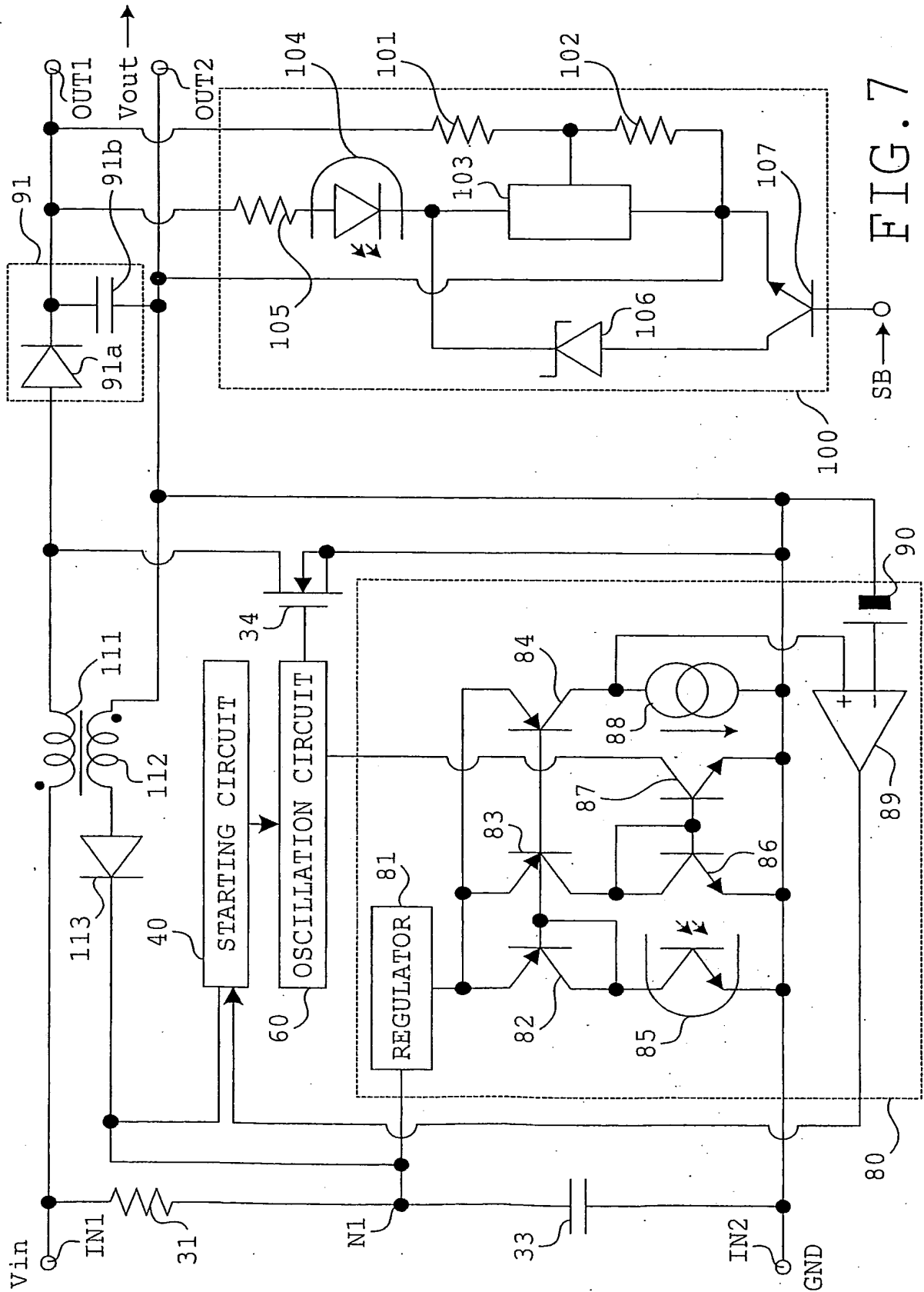


FIG. 8 PRIOR ART



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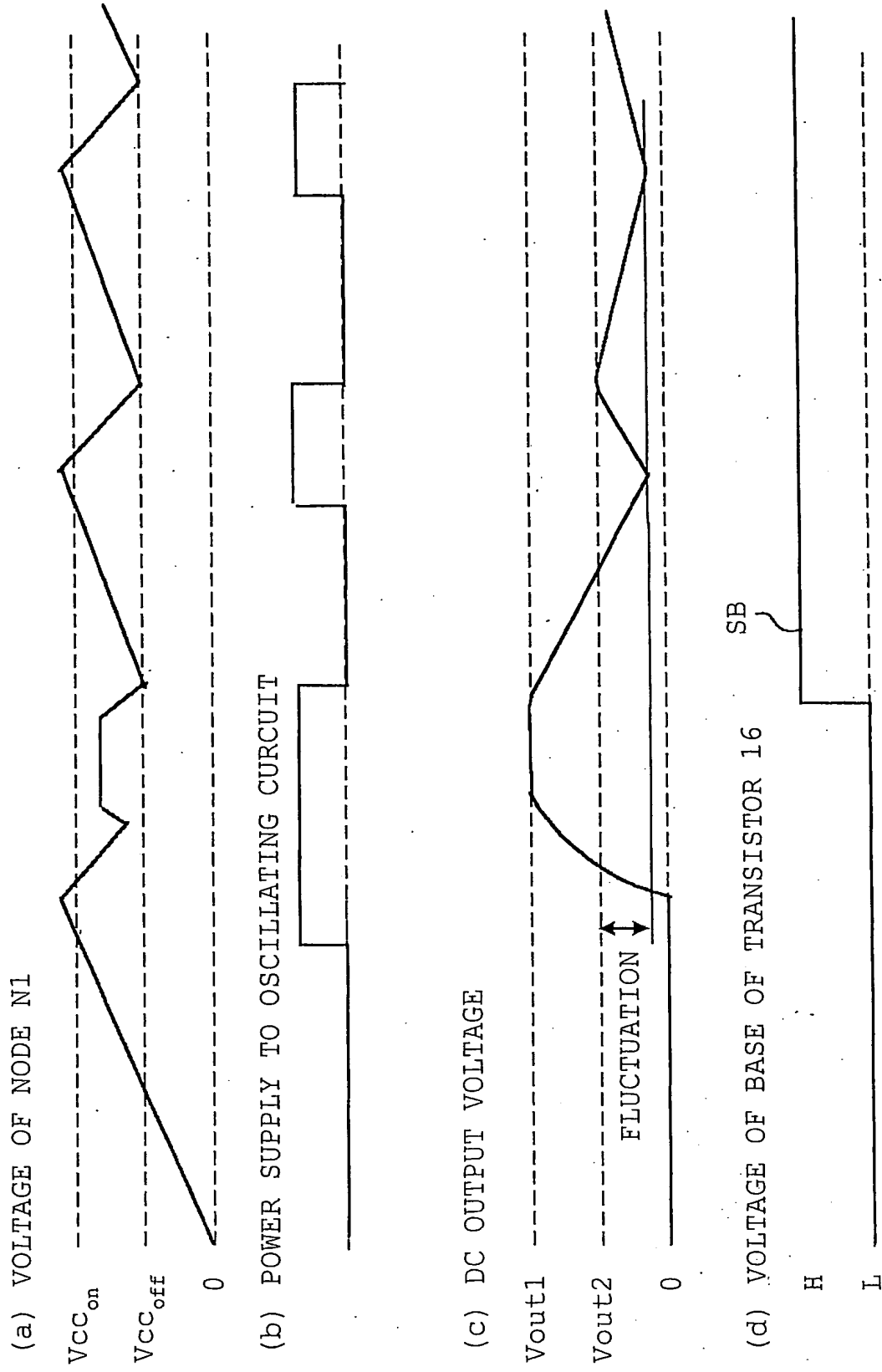


FIG. 9 PRIOR ART